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## Abstract

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Dielectric Barrier Discharge Plasma (DBD) is one of cold plasma type. In this research, DBD plasma was used for reducing Chemical Oxygen Demand (COD) on industrial rubber waste. In the environment field DBD plasma is used as a water treatment and reduction of gas emissions (COx, NOx, and HC). In the development DBD plasma can be used as a wastewater treatment technology. Plasma is formed when a high voltage is supplied to the two electrodes. Electrodes used for this study is made of stainless steel and copper wire wrapped around pyrex pipes. Plasma as wastewater treatment technology uses high-voltage AC 50 Hz. Plasma from dielectric barrier discharge occurs when the electrode is given a high voltage to ionize water into active species. Plasma are generated depends on the amount of high voltage, configuration and electrode materials used, and the distance between the electrodes with a high voltage source. Plasma reactor is operated by using a variation of voltage (10 kV, 11 kV, 12 kV and 13 kV) and contact time plasma with wastewater (5 minutes, 10 minutes, 15 minutes, 20 minutes and 25 minutes) with a batch system in processing wastewater to reduce amount of Chemical Oxygen Demand (COD) in wastewater processing natural rubber. Reducing COD contained in the waste because of the formation of the active species (O, OH, and O<sub>3</sub>) to degrade the organic matter in the wastewater of natural rubber industry. © 2016 IEEE.

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(COD) (Dielectric Barrier Discharge (DBD) Plasma) (Natural Rubber Industry) Wastewater

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Degradation of highconcentration simulated organic wastewater by DBD plasma

Wang, X.-Q., Li, X., Zhou, R.-W. (2019) Water Science and Technology

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Low latency network-on-chip router using static straight allocator - IEEE Conference Publication

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#### I. Introduction

Network-on-Chip (NoC) [1] provides a flexible and extensible inter-corecommunication infrastructure for many-core system-on-chips. However, due to multiple number of routers a packet has to traverse between a source and destination cores, as well as each individual router buffering, NoC-based systems canSigffinforConginateRecoding on munication latency. Reducing NoC communication latency is important as manycore based applications are highly sensitive to inter-core communication latency. However, designing a low latency NoC router can be a challenge.

## Authors

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E Contents

### I. Introduction

Surge arresters have been widely used to protect distribution and transmission transformers against lightning, switching overvoltage [1]. The non-linear voltage-current characteristic of surge arrester [2] lead they function as high impedances at normal operating voltages and become low impedances during overvoltage condition to release surge current from line to ground. The evolution of arrester technology has been characterized by the gradual improvement. Several different types of arresters are available such as gapped silicon carbide (SiC) and non-gapped zinc-oxide (ZitO). The performance of gapped arresters is determined by electrical and thermal properties of the ZnO block. Compared with SiC arresters, ZnO offer a protection closer to the ideal. However, the ZnO arresters are contain no gaps, the high leakage current can flow through the varistors at normal voltage level, which causes power losses and heat of ZnO elements. This condition may lead to the risk of thermal runaway [3] resulting in extensive damage of the ZnO arresters.

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#### I Introduction

Research on multi-user multiple-input multiple-output (MIMO) interference channels has been of great interest since interference channels include a majority of wireless communication models [1], [2]. In such systems, multiple terminals share the same frequency at the same time and, therefore, inter-user interference makes the channel capacity degradation. Optimal designs of the precoding and postcoding matrices for interference channels are mathematically challenging due to the nonconvexity associated with the optimization problems. Extensive studies have focused on designing the transmit beamformers to maximize the sum-rate of the systems [1], [3]. Alternatively, the mean square error (MSE) minir& igationton & beenee Repated of or the transceiver designs in MIMO broadcast channels [4] and in interference channels [5]. It was shown in [6] that the weighted MSE mimmization is equivalent to the sum-rate maximization when the weights are optimally chosen. Recently, interference alignment (IA) has been an emerging technique for interference mitigation in wireless networks [2], [7], [8]. In IA schemes, the transmitters cooperate each other to align their signals into a reduced dimension subspace at the unintended receivers and reserve the interference-free subspaces for the desired signals. Due to its effectiveness in dealing with interference, IA has been applied in various wireless networks [9]-[12].

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	I. Introd	luction							
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	-	spectrum. Cognitive radio evolved							
	(SDR) [1]. In order to become cognitive radio, SDR developed with the Sign in to Continue Reading ability to manage and optimize the spectrum and network resources to								
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